

VDOUT

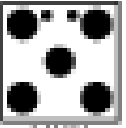
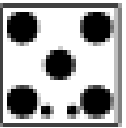
GND

GND

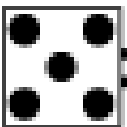
VLDOUT

VL

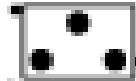
AINP



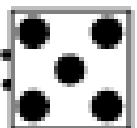
CLOCK



R10



CLOCK4



VCLK



GND

